



THE STUDY OF IMPACT THIRD HARMONIC BY SALIENT POLE SYNCHRONOUS GENERATOR

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ABSTRACT

This research focuses on the study of impact third harmonic salient pole synchronous generator by using sinusoidal PWM (SPWM) technique. The methods used for this research was the analysis method of an SPWM of AC signal, analysis of a Voltage-source inverter with SPWM output and Fourier analysis of an SPWM. These methods were compared by discussing their ease of implementation, by analysing the output of the third harmonic of various output voltages and currents, and the third harmonic distortion (THD). The simulation results showed that the SPWM techniques had higher total harmonic distortion as compared to the SPWM coding technique.

Keywords: sinusoidal PWM, third harmonic distortion, matlab coding and simulink, voltage source inverter and fourier analysis.

INTRODUCTION

Harmonic in a power system existed since the discovery of the alternating current (AC) system back in the 1980s when the first alternator was first built [1],[4]. The imperfect sinusoidal voltage produced by AC generator or distorted voltage/current resulted from a non-linear load that contained various harmonic levels when analysed using Fourier series/transform.

A harmonic is a signal or wave with an integral (whole-number) multiple frequencies of some reference signal or wave in addition to the energy at a fundamental frequency [1], [2]. A signal will become a perfect sine wave when all of the energy is contained at the fundamental frequency. But, if only some of the energy is contained in the harmonics, the signal will not become a perfect sine wave because of some waveforms that contain large amounts of energy at harmonic frequencies. For example; square waves, saw tooth waves, and triangular waves. The third harmonic has caused some power quality problem in the neutrality of the distribution system [3]. The salient pole synchronous generator has been recognized as one of the third harmonic sources [5].

MATERIALS AND METHODS

The Pulse Width Modulation is a powerful technique to control analogue circuits that contain the power to be sent to a load. PWM is a way to encode analogue signal levels digitally, meanwhile, a duty cycle is defined as the ratio of (ton/T) where T is the period in seconds [2]. PWM techniques are aimed at providing a better controllable output voltage and reduction of harmonics [1]. This study, however, dealt with the comparison of third harmonic analyses performed by some of the PWM techniques using the FFT tool of Simulink in MATLAB and undertaking a few of them by inferring the best technique to be used. The main advantages of Simulink are flexible open source implementation working on .mdl and .slx Simulink models and strong mathematical base on some appealing structuring concept. The disadvantages are the tools was not initially meant and

designed for code generation and lack many desirable features of a programming language. The sinusoidal pulse-width modulation (SPWM) technique produces a sinusoidal waveform by filtering an output pulse as the waveform is of varying width [3]. A high-switching frequency leads to a better filtered sinusoidal output waveform. The desired output voltage is achieved by varying the frequency and amplitude of a reference or modulating voltage. The variations in the amplitude and frequency of the reference voltage change the pulse-width patterns of the output voltage but maintain the sinusoidal modulation. A low-frequency sinusoidal modulating signal, then, is compared with a high-frequency triangular signal called the carrier signal. The switching state changes when the sine waveform intersects the triangular waveform. The crossing positions are to determine the variable switching times between states as shown in Figure-1 below in a Matlab Simulink flow chart.

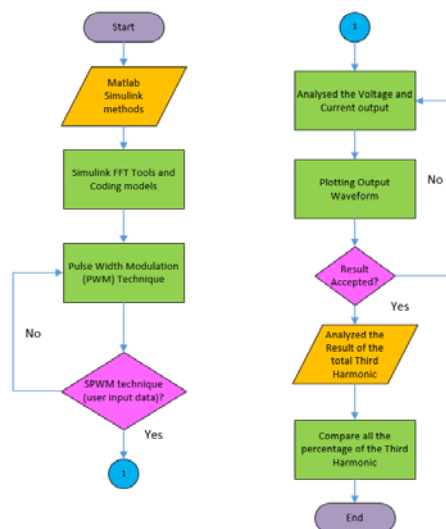


Figure-1. Matlab Simulink flow chart.



The switching signal generation model for SPWM in the simulation and coding are also shown in the figure. This research used the analysis of Sinusoidal PWM of an AC Signal, analysis of a voltage-source inverter with Sinusoidal PWM output and Fourier analysis of a Sinusoidal PWM signal. The fundamental frequency is 50Hz or 60Hz. The amplitude modulation index is 0-1. Figure 2 shows the circuit diagram of Simulink FFT tools for Fourier Analysis of SPWM. This circuit shows the connection of the parameters in order to find the total for the Third Harmonic Distortion. Table-1 shows the data from the laboratory.

Table-1. Data of generator.

Voltage	415
Frequency	50Hz
KvA	1265
kW	1012
Efficiency %	95.4
kW input	10310000000

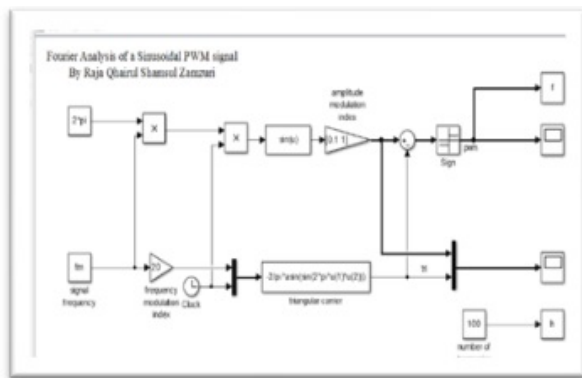


Figure-2. Simulink FFT tools Fourier analysis of SPWM circuit diagram.

RESULT

The results of the three methods are analyzed and discussed in the graphs. The result analysis for SPWM of an AC signal, analysis of a voltage source inverter with SPWM output and Fourier analysis of an SPWM signal is shown in signal waveform, table, and graph.

Method 1: Analysis of SPWM of an AC signal (Coding)

Figure-3 shows the result of the command window of the SPWM evaluated coding analysis of an AC signal where it displays the beginning (alpha), the end (beta), and the width of each output pulse. It also shows the RMS value of the output voltage, the RMS value of the output voltage fundamental component, the RMS value of the load current, the RMS value of the supply current, the output voltage of Third Harmonic Distortion, the output current of Third Harmonic Distortion and the power factor for this type of coding technique.

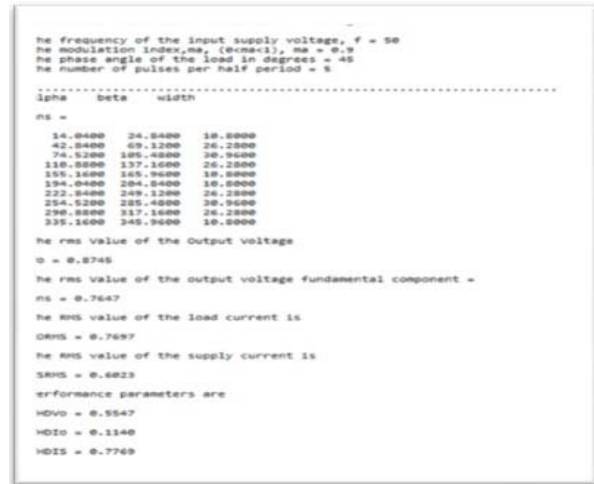


Figure-3. Result SPWM from coding simulation at command window.

Figure-4 and 5 show the waveform of the output voltage, output current, supply current and harmonic order pulses of SPWM. In figure 4, the waveform shows the output voltage waveform V_{in} , the triangular carrier signal V_t , the modulating signal, and the output voltage waveform V_{out} . And, Figure 5 shows the waveform pulses of an output voltage, output current, supply current and the third harmonic contents of the values.

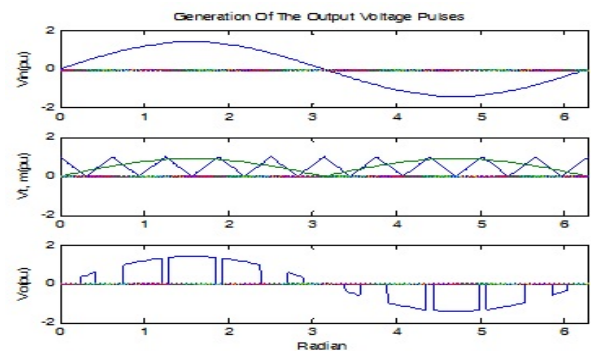


Figure-4. Generation of the output voltage pulses SPWM.

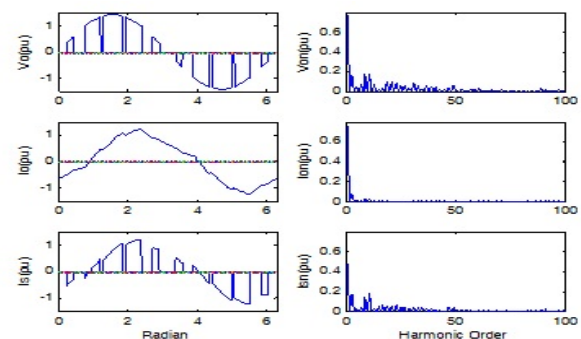


Figure-5. Generation of the output voltage, output current, supply current and harmonic order pulses of SPWM.



Table-2 shows the value of the input modulation index from 0.1 until 0.9 with a modulation index range from 0-1. The table also shows the output of the SPWM coding with the RMS value of the output Voltage, the RMS value of the output voltage fundamental component, the RMS value of the load current, the RMS value of the supply current, the output voltage of Third Harmonic Distortion, the output current of Third Harmonic Distortion, the supply current of Third Harmonic Distortion and the power factor for this type of coding technique. From the table, the average output voltage and current of Third Harmonic Distortion are shown.

Table-2. Table output of SPWM coding.

Ma/Output	Vo (v)	Vo F Component (v)	CORMS (A)	CSRMS (A)	THD Vo %	THD Io %	THD Is %	Power Factor
0.1	0.2835	0.0804	0.0825	0.0206	3.3829	0.2554	3.8778	0.1162
0.3	0.4996	0.2496	0.2553	0.115	1.7338	0.2139	2.0004	0.2094
0.5	0.6523	0.4255	0.4317	0.2529	1.162	0.1724	1.3825	0.2725
0.7	0.769	0.5913	0.5971	0.4111	0.8314	0.1398	1.0412	0.3229
0.9	0.8745	0.7647	0.7697	0.6023	0.5547	0.114	0.7769	0.3718
THD Average					1.53296	0.1791		

I. Method 2: Analysis of voltage source inverter with SPWM output (Coding)

Figure-6 shows the result of the command window of the evaluated coding of Voltage Source Inverter with SPWM output where it displays the beginning (alpha), the end (beta) and the width of each output pulse. It also shows the RMS value of the output Voltage, the RMS value of the output voltage fundamental component, the RMS value of the load current, the RMS value of the supply current, the average value of the supply current, the output voltage of Third Harmonic Distortion and the output current of Third Harmonic Distortion for this type of coding technique.



Figure-6. Result VSI SPWM from coding simulation at command window.

Figure-7 and 8 show the waveform of the output voltage, output current, supply current and harmonic order pulses of SPWM. In Figure-4, the waveform shows the triangular carrier signal V_t , the modulating signal, and the output voltage waveform V_{out} . And, Figure-5 shows the waveform pulses of an output voltage, output current, supply current and the third harmonic contents of the values.

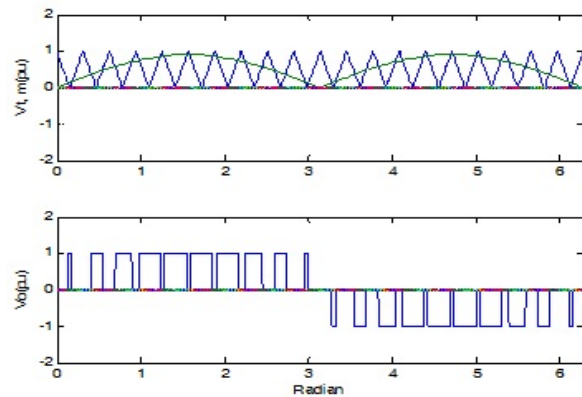


Figure-7. Generation of the output voltage pulses voltage source inverter.

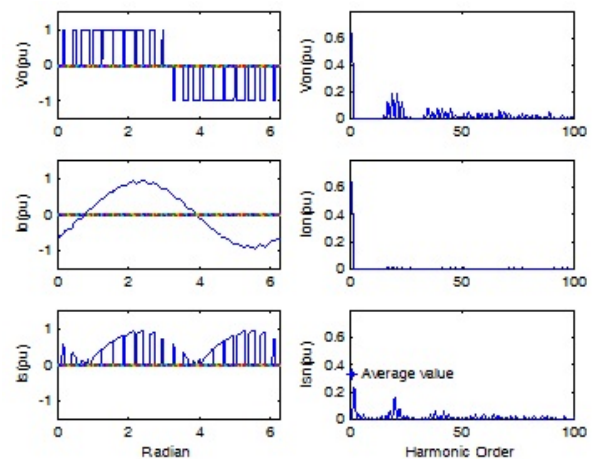


Figure-8. Generation of the output voltage, output current, supply current and harmonic order pulses of voltage source inverter.

Table-3 shows the value of the input modulation index from 0.1 until 0.9 with a modulation index range from 0-1. The table also shows the output of the voltage source inverter SPWM coding with the RMS value of the output voltage, the RMS value of the output voltage fundamental component, the RMS value of the load current, the RMS value of the supply current, the average value of the supply current, the output voltage of third harmonic distortion and the output current of third harmonic distortion for this type of coding technique. From the table, the average output voltage and current of third harmonic distortion are shown.

**Table-3.** Table output of VSI SPWM coding.

Ma/Output	Vo (v)	Vo F.Component (v)	CORMS (A)	CSRMS (A)	CSAV (A)	THD Vo %	THD Io %
0.1	0.209	0.0687	0.0692	0.0169	0.0037	3.4846	0.1203
0.3	0.4382	0.2112	0.2122	0.0926	0.0362	1.8173	0.0967
0.5	0.5657	0.3537	0.3547	0.2004	0.1011	1.2481	0.0749
0.7	0.6663	0.491	0.4917	0.3273	0.1946	0.9176	0.0552
0.9	0.7603	0.6377	0.6381	0.4844	0.3289	0.6492	0.0369
THD Average						1.62336	0.0768

II. Method 3: Fourier analysis of an SPWM signal (Simulink FFT Tools)

Figure-9 shows the output result in the command window from a running circuit diagram of Simulink FFT tools for Fourier analysis of SPWM. The figure shows the function of an output voltage with the order of the third harmonic, the amplitude, the phase values, the RMS value of output voltage, and the output voltage of third harmonic distortion. The figure also shows the function of the output with the current order of the third harmonic, the amplitude, the phase values, the RMS value of output current, and the output current of third harmonic distortion.

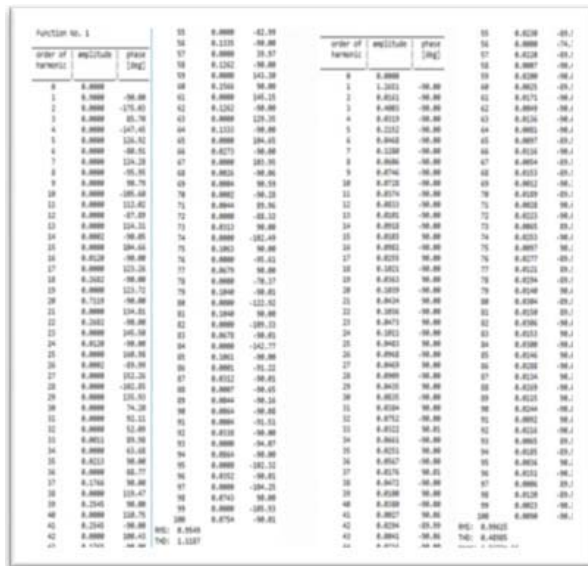
**Figure-9.** Result Fourier analysis from Simulink at command window.

Figure-10 and 11 shows the waveform of the output voltage, output current and harmonic order pulses of Fourier analysis of SPWM. In Figure-10, the waveform shows the periodic signal of the output voltage and the third harmonic Fourier components discrete signal. And, Figure-11 shows the waveform periodic signal of the output current and the third harmonic Fourier components discrete signal.

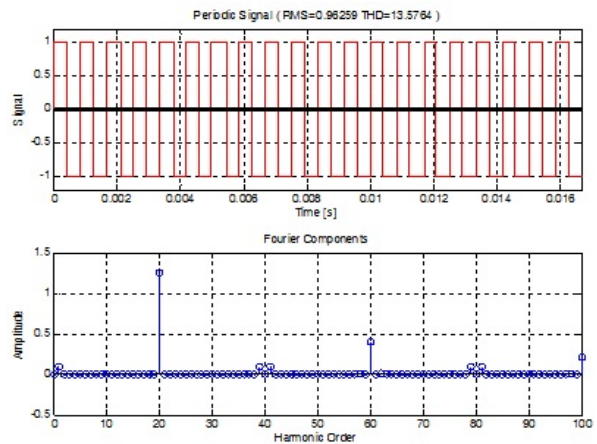
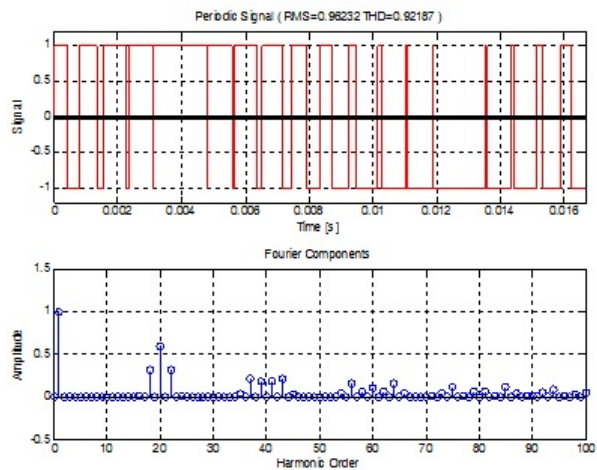
**Figure-10.** Fourier analysis of SPWM signal waveform for THD Vo.**Figure-11.** Fourier analysis of SPWM signal waveform for THD Io.

Table-4 shows the value of the input modulation index from 0.1 until 0.9 with a modulation index range from 0-1. This table also shows the output of the Fourier analysis of SPWM Simulink with the RMS value of the output voltage, the RMS value of the load current, the output voltage of third harmonic distortion and the output current of third harmonic distortion for this type of coding technique. From the table, the average of the output voltage and current of third harmonic distortion are shown.

Table-4. Table output of VSI SPWM coding.

Ma/Output	VRMS (v)	THD Vo %	Mean	IRMS (A)	THD Io %	Mean
[0.1]	0.96259	13.5764	-2.97E-14	0.96232	0.92187	-3.24E-14
[0.3]	0.95816	4.4047	-1.69E-14	0.98727	0.55946	-3.18E-14
[0.5]	0.95759	2.5171	-2.23E-14	0.99221	0.51295	-6.49E-15
[0.7]	0.95898	1.6594	-6.39E-14	0.99118	0.48595	5.04E-15
[0.9]	0.9549	1.1187	1.31E-14	0.99615	0.48985	4.56E-16
THD Average		4.65526			0.594016	



III. Comparison of the Third Harmonic Distortion (THD) from the Three Methods

Table-5 shows the average value of the voltage output from the analysis of spwm, analysis of voltage source inverter SPWM and Fourier analysis of SPWM. From the table, the comparisons of the output voltage third harmonic distortion from the three methods are shown. The result shows a higher value of output voltage third harmonic distortion from fourier analysis of SPWM Simulink technique.

Table-5. Table average THD of voltage output.

Ma/Output	THD Vo % Coding 1	THD Vo % Coding 2	THD Vo % Simulink
0.1	3.3829	3.4846	13.5764
0.3	1.7338	1.8173	4.4047
0.5	1.162	1.2481	2.5171
0.7	0.8314	0.9176	1.6594
0.9	0.5547	0.6492	1.1187
THD Average	1.53296	1.62336	4.65526

Figure-12 shows the graph average third harmonic distortion of voltage output. The graph shows the fourier analysis SPWM Simulink technique with a higher percentage from all three methods.

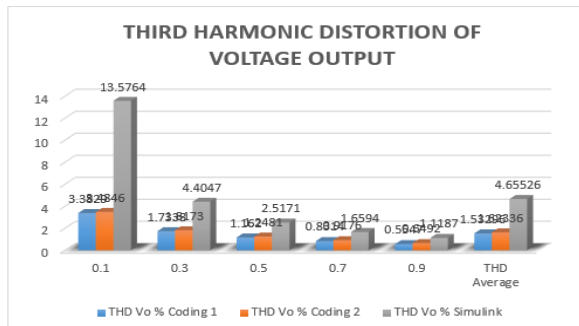


Figure-12. Graph average THD of voltage output.

Table-6 shows the average value of the current output from the analysis of SPWM, analysis of voltage source inverter SPWM and fourier analysis of SPWM. From the table, the comparisons of the output current third harmonic distortion of the three methods are shown. The result shows a higher value of output current third harmonic distortion from fourier analysis of SPWM Simulink technique.

Table-6. Table average THD of current output.

Ma/Output	THD Io % Coding 1	THD Io % Coding 2	THD Io % Simulink
0.1	0.2554	0.1203	0.92187
0.3	0.2139	0.0967	0.55946
0.5	0.1724	0.0749	0.51295
0.7	0.1398	0.0552	0.48595
0.9	0.114	0.0369	0.48985
THD Average	0.1791	0.0768	0.594016

Figure-13 shows the graph average third harmonic distortion of current output. The graph shows the fourier analysis SPWM Simulink technique with a higher percentage from all three methods.

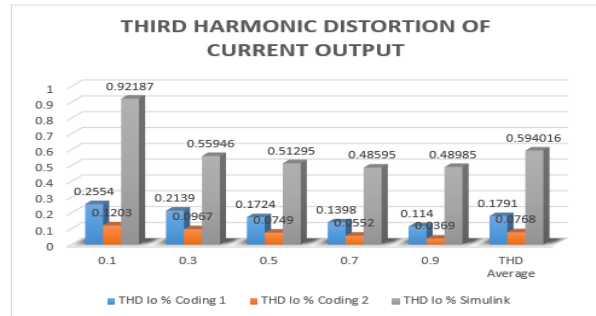


Figure-13. Graph average THD of current output.

From the simulation Simulink and coding result, the analysis of SPWM of AC signal, analysis of a voltage-source inverter with SPWM output and fourier analysis of an SPWM signal are shown in the respective figure. The three methods of an output voltage and current waveforms are shown in the figure. THD measurement of the corresponding three methods output voltage and current are also shown in the figure with the amplitude modulation index from 0.1 until 0.9. The simulation of fourier analysis SPWM shows a higher percentage of the average THD Voltage output that is 4.66% as compared to coding SPWM and coding voltage-source inverter SPWM as shown in Figure-12. The simulation of fourier analysis SPWM shows a higher percentage of the average THD current output that is 0.594% as compared to coding SPWM and coding voltage-source inverter SPWM as shown in Figure-13. The simulation methods also show a better and accurate performance of the output and current waveform from any other methods.

CONCLUSIONS

In this research, the comparative analysis of the SPWM of an AC signal, analysis of a voltage-source inverter with SPWM output, and fourier analysis of an SPWM signal are performed and their performances have been presented in terms of amplitude modulation index, switching frequency and inverter input voltage. From the simulation Simulink and coding results, it can be said that SPWM is capable of performing in over-modulation region and in high-switching frequency application. simulation Simulink of SPWM also shows a better THD Vo and Io THD that remain constant with the variation of the inverter input voltage for SPWM. The current THD continuously decreases, whereas voltage THD is proportional in order to input voltage for SPWM. Finally, it can be concluded that Simulink SPWM is able to enhance the fundamental output with better quality with lesser THD as compared to other output. Problem, it is important to conduct a study on how to compensate the effect of the dead time in order to increase the performance of the drive systems.

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